

#### SysML to AADL Bridge



# Automating the Translation of SysML into AADL for Analysis and Refinement

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This material is based upon work supported by the U.S. Army Development Command (DevCOM), U.S. Army Combat Capabilities Development Command Aviation & Missile Center (DEVCOM AvMC), ATTN: Security Office, Bldg 401, Lee Blvd, Fort Eustis, VA 23604-5577 under contract no. W911W6-17-D-0003. Any opinions, findings, and conclusions or recommendations expressed in this material are those of the author(s) and do not necessarily reflect the views of the U.S. Army.

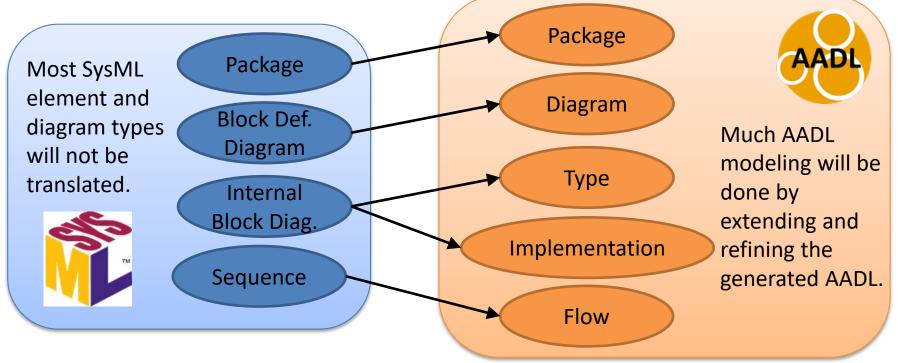


#### Agenda

- Design goals & supported workflows
- Demo
  - Using the profile
  - Code generation
  - Analysis and round-trip engineering
  - Model refinement & integration
- Ongoing work (behavior & property mapping)
- Questions

### Targeted Portions of SysML Only

 Allow users to annotate portions of SysML models using an AADL Profile and automatically translate those portions of the SysML model into AADL.



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#### **Clear Semantics**

- Transition SysML specification from system engineering phase of development into standardized AADL semantics for ACVIP/AADL virtual integration analysis.
- Provide a common (across multiple programs, contractors) AADL Profile based on the AADL standard that is amenable to extension for additional AADL properties and annexes (for integration of additional analysis tools).

Provided SysML AADL Profiles:

- Core AADL stereotypes and properties
- Extension profiles for ARINC 653, MILS, SESSAF (STPA), FASTAR, MADS MagicDraw/Cameo Enterprise Architecture and Sparx Enterprise Architect

#### Adventium Labs Mapping of Core AADL Stereotypes (Excerpt from Modeling Guidelines)

AADL stereotype	SysML element	Generated AADL declaration(s)	AS5506C
AADL_Package	package	package in a file	4.2
AADL_Abstract	block	abstract type + implementation	4.6
AADL_Data	block	data type + implementation	5.1
AADL_Subprogram	block	subprogram type + implementation	5.2
AADL_Thread	block	thread type + implementation	5.3
AADL_Thread_Group	block	thread group type + implementation	5.4
AADL_Process	block	process type + implementation	5.5
AADL_Processor	block	processor type + implementation	6.1
AADL_Virtual_Processor	block	virtual processor type + implementation	6.2
AADL_Memory	block	memory type + implementation	6.3
AADL_Bus	block	bus type + implementation	6.4
AADL_Virtual_Bus	block	virtual bus type + implementation	6.5
AADL_Device	block	device type + implementation	6.6
AADL_System	block	system type + implementation	7.1
AADL_Feature	port	feature (in a type declaration)	8.1
AADL_Feature_Group	port	feature group (in a type declaration)	8.2
AADL_Feature_Group	interfaceBlock	feature group type + implementation	8.2
AADL_Access	port	provides/requires access (in a type declaration)	8.6-8.8
AADL_Flow	interaction	flow source, path, sink, end-to-end	10

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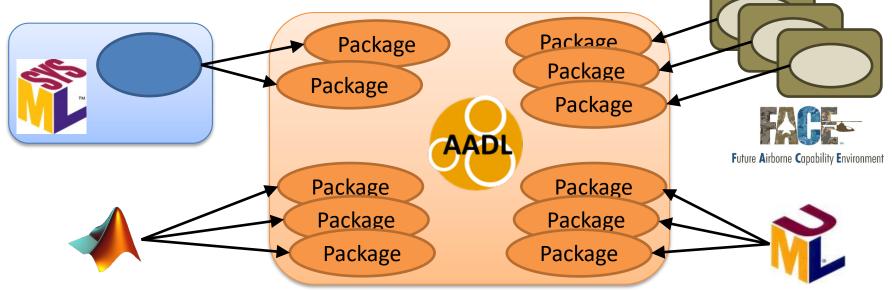
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#### Workflow Support

- Virtual integration of computer system architecture model occurs in an AADL modeling environment.
- Modularized, textual, BNF-based exchange formats have been proven very useful for model exchange and configuration management.



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### Modeling Style Support

- SysML standards and conventions, natural for SysML aficionados.
- AADL standards and conventions, natural for AADL aficionados.
- Clear mapping from stereotyped SysML elements with tagged value properties to the AADL generated from those elements and properties.

Provided Documentation:

- SysML AADL Profile and Modeling Guidelines
- SysML example models



#### SysML AADL Profile and Modeling Guideline Topics

- Names and Name Spaces
- Allocation and Binding Relations
- Packages, Blocks, Types, and Implementations
- Tagged Values and Properties
- ✓ Ports, Features, and Connections
- ✓ Interactions and Flows
- ✓ Annex Profiles

- Not covered today
- ✓ At least briefly discussed

# (Quick Shout Out/Thank You)

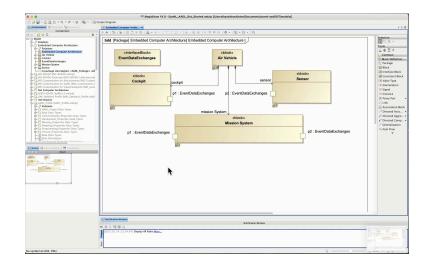
- Our work was accomplished with oversight by a SysML AADL Working Group consisting consisting of Army Aviation system integrators and suppliers.
- Other work we looked at:
  - MARTE

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- Open Archive Toulouse Archive Ouverte (OATAO)
- ExSAM Profile, 2011 (University of Oslo, Norway)
- SCADE Architect to AADL translator
- Rockwell Collins SysML to AADL translator v1.1 2016
- SLICED AADL Profile for UML state machine diagrams

### Selecting Elements for Translation

- Using AADL Profile to select Packages and Components for translation.
- Annotating the Block Definition Diagram.



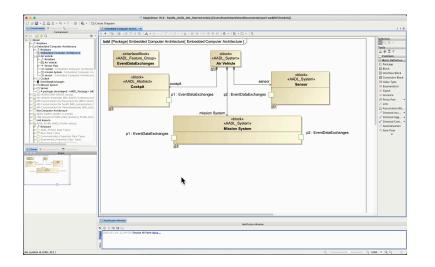
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#### Annotating with Tag Values

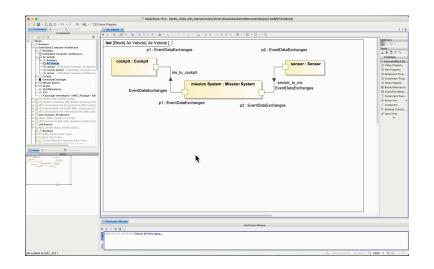
- Adding detailed port and connections information using Stereotype Tags.
- Annotating the Internal Block Diagram.





#### Adding End to End Flows

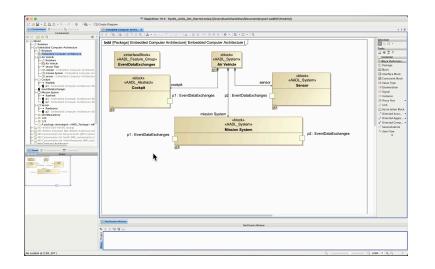
• Creating a sequence diagram to add a flow through existing components.





#### **Round-trip Engineering**

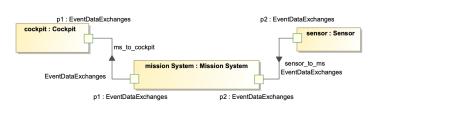
- Generation of AADL project.
- Mapping back from AADL to SysML using GUIDs.

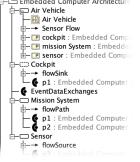




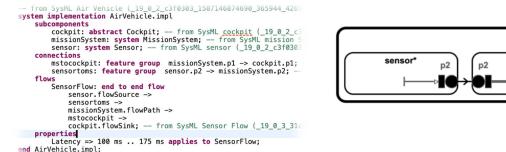


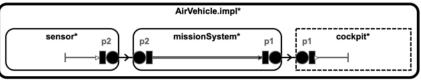
- Detailed AADL Analysis: where should additional property values and model details be added?
  - Option 1: Expand SysML model with refinements, new components, new tags/properties.





Option 2: Refine generated AADL model.





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## **Option 1: SysML Refinement**

Example of use: Want to provide the property at the SysML level.

- Generalization/Inheritance refinement all supported by the translator.
- Create custom profiles for a missing AADL Property Sets.
  - Detailed instructions in provided documentation.
  - Any tag defined in a profile named "AADL\_<something>\_Profile" will be translated into an AADL property if a SysML model element has that tag.

Provided Example Model:

• Architecture\_Refinement\_Example

Provided SysML AADL Profiles:

- Core AADL stereotypes and properties
- Extension profiles for ARINC 653, MILS, SESSAF (STPA), FASTAR, MADS MagicDraw/Cameo Enterprise Architecture and Sparx Enterprise Architect

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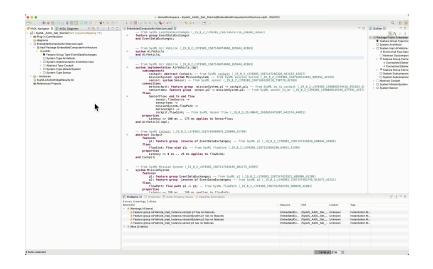
#### Adventium **Options 2: AADL Refinement**

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Example of use: Complex properties defined during the embedded design phase with added analyses as the fidelity of the model increases.

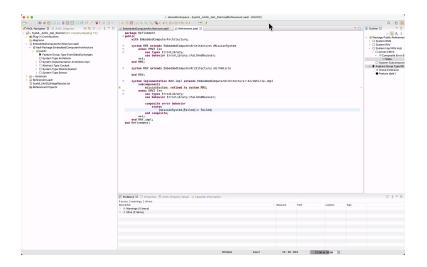
 Refine Mission System & Air Vehicle to include Error Modeling using EMV2 annex.





#### **AADL Integration**

 Integrate existing project feature types into generated AADL model.



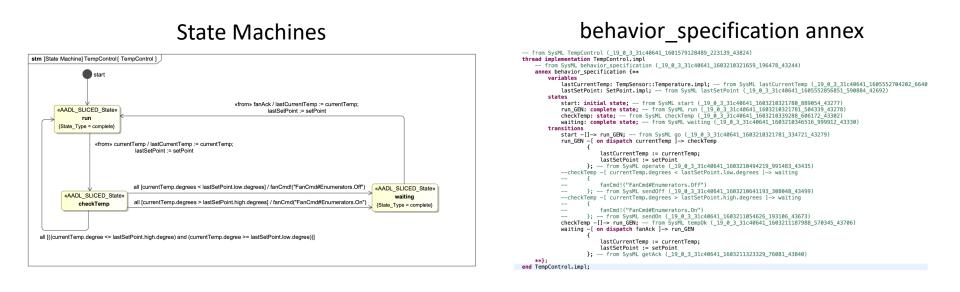
## Highlighted Ongoing Work (1 of 2)

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• Behavior & Annexes under GUMBO (Grand Unified Modeling of Behavioral Operators) SIBR (Adventium and Kansas State).



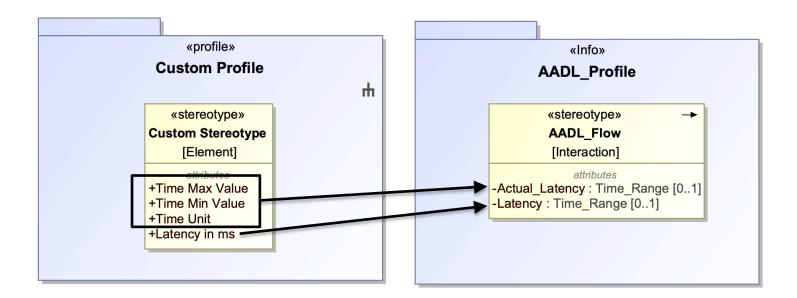
# Highlighted Ongoing Work (2 of 2)

- Mapping other existing profile tag values or properties to generated AADL properties.
- User must ensure mapping is semantically correct.

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#### **Unsupported AADL Features in Bridge**

- Connection bindings that are ordered lists (routings)
- System operating modes
- Prototypes

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- Subprogram parameters
- Subprogram call sequences
- Annex sub-languages (e.g., EMV2 Annex, Behavior Specification Annex)\*
- Multiple implementations for a type must be declared as extensions to both the type and the implementation.

\*Ongoing work



#### Questions?

#### https://camet.adventium.com

#### • SysML AADL Profiles

- Core AADL stereotypes and properties
- Extension profiles for ARINC 653, MILS, SESSAF (STPA), FASTAR, MADS MagicDraw/Cameo Enterprise Architecture and Sparx Enterprise Architect

#### Documentation

- SysML AADL Profile and Modeling Guidelines
- SysML example models
- SysML to AADL Bridge Tool

MagicDraw/Cameo Enterprise Architecture and Sparx Enterprise Architect