GraphBLAS: A Programming Specification for Graph Analysis

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In collaboration with:

MIT/LL, LBNL, Intel, IBM, UC Davis, Indiana, and many others.



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Summary



Graph analysis is *important* and *pervasive* in the DoD community.

GraphBLAS Forum (a world-wide consortium of researchers)

- Government/FFRDCs, academia, and industry
- Goal: application programming specification (API) for graph analysis
- GraphBLAS: Graph <u>Basic Linear Algebra</u> Subprograms

SEI contributions

- Member of the Graph BLAS Forum
- Member of the C API Specification committee
- Early implementation of a C++ library: GraphBLAS Template Library (opensource)

Graph Analysis is Important and Pervasive





Common Goal: Detection of subtle patterns in massive graphs

Slide credit: Jeremy Kepner, et al. "Mathematical Foundations of the GraphBLAS", IEEE HPEC, Sept. 2016.



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Common Operations

- Finding Neighbors
- Shortest Paths
- Clustering
- "Important" nodes
 - Influencers
 - Bottlenecks
 - Outliers



W. W. Zachary, An information flow model for conflict and fission in small groups, Journal of Anthropological Research 33, 452-473 (1977).

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Graphs as Matrices





Graphs are represented as adjacency matrices that have *irregular* and *sparse* structure.



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SEI Research Review 2016

Graph Operations as Matrix Operations





- Matrix multiply \rightarrow find neighbors (most important primitive)
- Used in breadth-first traversal, shortest paths, and many others
- Sparsity and irregularity of matrix structure is a barrier to high performance

Today's Computing Landscape





Intel Xeon E5-2699v3 662 Gflop/s, 145 W 18 cores, 2.3 GHz 4-way/8-way AVX2



IBM POWER8 384 Gflop/s, 200 W 12 cores, 4 GHz 2-way/4-way VMX/VSX



NVIDIA Tesla P100 10.6 Tflop/s, 250 W 3584 cores, 1.48 GHz 64-way SIMT



Intel Xeon Phi 1.2 Tflop/s, 300 W 61 cores, 1.24 GHz 8-way/16-way LRBni





Qualcomm Snapdragon 810 10 Gflop/s, 2 W 4 cores, 2.5 GHz A330 GPU, V50 DSP, NEON

Software Engineering Institute

Intel Atom C2750 29 Gflop/s, 20 W 8 cores, 2.4 GHz 2-way/4-way SSSE3

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Dell PowerEdge R920 1.34 Tflop/s, 850 W 4x 15 cores, 2.8 GHz 4-way/8-way AVX



IBM BlueGene/Q 10 Pflop/s, 8 MW 48k x 16 cores, 1.6 GHz 4-way QPX

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Slide credit: Franz Franchetti, "SPIRAL: Automated Code Generation of Performance Libraries," 2016.

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Separation of Concerns



Separate the complexity of graph analysis from the complexity of hardware systems:



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Graph Primitives Using Linear Algebra



Standards for Graph Algorithm Primitives

Tim Mattson (Intel Corporation), David Bader (Georgia Institute of Technology), Jon Berry (Sandia National Laboratory), Aydin Buluc (Lawrence Berkeley National Laboratory), Jack Dongarra (University of Tennessee), Christos Faloutsos (Carnegie Melon University), John Feo (Pacific Northwest National Laboratory), John Gilbert (University of California at Santa Barbara), Joseph Gonzalez (University of California at Berkeley), Bruce Hendrickson (Sandia National Laboratory), Jeremy Kepner (Massachusetts Institute of Technology), Charles Leiserson (Massachusetts Institute of Technology), Andrew Lumsdaine (Indiana University), David Padua (University of Illinois at Urbana-Champaign), Stephen Poole (Oak Ridge National Laboratory), Steve Reinhardt (Cray Corporation), Mike Stonebraker (Massachusetts Institute of Technology), Steve Wallach (Convey Corporation), Andrew Yoo (Lawrence Livermore National Laboratory)

"It is our view that the state of the art in constructing a large collection of graph algorithms in terms of linear algebraic operations is mature enough to support the emergence of **a standard set of primitive building blocks**. This paper is a position paper defining the problem and **announcing our intention to launch an open effort to define this standard**."

Presented at the IEEE High Performance Extreme Computing Conference. Waltham, MA, Sept. 2013.

GraphBLAS Forum



FFRDCs

- MIT/Lincoln Labs*
- Lawrence Berkeley NL* †
- CMU/Software
 Engineering Institute⁺
- Pacific Northwest NL
- Sandia NL

Industry

- Intel^{*}⁺
- IBM⁺
- NVIDIA
- Hauwei
- Reservoir Labs
- Galois
- Mellanox
- and others

Academia

- UC Santa Barbara*, Davis*, Berkeley
- Georgia Tech*
- Karlsruhe (KIT)*
- CMU
- Indiana U.
- MIT
- U. Washington
- and others

Separation of Concerns

GOAL: write once, run everywhere (with help from hardware experts).



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GraphBLAS Primitives



Operation	Description
mxm, mxv, vxm	Perform matrix multiplication (e.g., breadth-first traversal)
eWiseAdd, eWiseMult	Element-wise <i>addition</i> and <i>multiplication</i> of matrices (e.g., graph union, intersection)
extract	Extract a sub-matrix from a larger matrix (e.g., sub-graph selection)
assign	Assign to a sub-matrix of a larger matrix (e.g., sub-graph assignment)
apply	Apply <i>unary function</i> to each element of matrix (e.g., edge weight modification)
reduce	Reduce along columns or rows of matrices (vertex degree)
transpose	Swaps the rows and columns of a sparse matrix (e.g., reverse directed edges)
buildMatrix	Build an matrix representation from row, column, value tuples
extractTuples	Extract the row, column, value tuples from a matrix representation



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Operation

mxv (vxm)

eWiseMult

eWiseAdd

transpose

extract

assign

buildMatrix

extractTuples

apply

reduce (row)

mxm

GraphBLAS Primitives: The Math



Mathematical Description	Outputs	Inputs	
C(¬M) ⊕= A ^T ⊕.⊗ B ^T	С	¬, M , ⊕, A, T, ⊕.⊗, B, T	LAS 2016.
c(¬m) ⊕= A ^T ⊕.⊗ b	С	¬, m , ⊕, A, T, ⊕.⊗, b	iraphB), July
$C(\neg M) \bigoplus = A^T \otimes B^T$	С	¬, M , ⊕, A, T, ⊗, B, T	f the G (AN16
$\mathbf{C}(\neg \mathbf{M}) \bigoplus = \mathbf{A}^{T} \bigoplus \mathbf{B}^{T}$	С	¬, M , ⊕, A, T, ⊕, B, T	ation o eeting
c(¬m) ⊕= ⊕ _j A ^T (:,j)	С	¬, m , ⊕, A, T, ⊕	ementa nual Mo
$C(\neg M) \bigoplus = f(A^{\intercal})$	С	¬, M , ⊕, A, T , <i>f</i>	d Imple M Anr
C(¬M) ⊕= A [⊤]	С	¬, M , ⊕, A (T)	ign ano)," SIA
C(¬M) ⊕= A ^T (i,j)	С	¬, M , ⊕, A, T , i, j	, "Desi (GBTL
C(¬M) (i,j) ⊕= A [⊤]	С	¬, M , ⊕, A, T , i, j	, et al.
C(¬M) ⊕= S ^{mxn} (i,j,v,⊕)	С	¬, M, ⊕ , ⊕, m, n, i, j, v	cMillan Mate Li
$(\mathbf{i},\mathbf{j},\mathbf{v}) = \mathbf{A}(\neg \mathbf{M})$	i,j,v	¬, M, A	S. Mc Temp

Notation: i,j – index arrays, v – scalar array, m – 1D mask, other bold-lower – vector (column), M – 2D mask, other bold-caps – matrix,

T – transpose, \neg - structural complement, \oplus monoid/binary function, \oplus . \otimes semiring, blue – optional parameters, red – optional modifiers



GraphBLAS Primitives: The Code

$C(\neg M) \bigoplus = A^{\mathsf{T}} \bigoplus . \otimes B^{\mathsf{T}}$

GrB_Info GrB_mxm(GrB_Matrix	*C,	//	output
const GrB_Matrix	Mask,		
const GrB_BinaryFunctio	n accum,		
const GrB_Semiring	op,		
const GrB_Matrix	A,	//	input matrix
const GrB_Matrix	В	//	input matrix
[, const Descriptor	<pre>desc]);</pre>		

Common Elements

- Matrices (C, Mask, A, and B) are opaque data structures defined by the library implementers.
- Destination (C) is first.
- Output mask (M) specifies which output elements can assigned (optional).
- Accumulation function (accum) allows operation to combine with existing values (optional).
- Descriptor can specify transpose of input matrices or logical complement of the output mask.

The GraphBLAS Forum

- Meeting since 2014
- Teleconferences once per month (C API group more often)
- Forum meetings open to the public:
 - May: IEEE IPDPS (Symposium) Graph Algorithms Building Blocks Workshop
 - Sept.: IEEE HPEC (Conference) GraphBLAS BoF
 - Nov.: IEEE/ACM Supercomputing (Conference) GraphBLAS Working Group
- Status and next steps:
 - Primitives established
 - C Application Programming Interface (API) First draft review in August
 - Targeted release: November 2016 at Supercomputing
- For more information: http://graphblas.org

Current and Future Work

- Continue work on C++ API from the open-source GraphBLAS Template Library (GBTL) [collaboration with Indiana/PNNL]
- Line Project (FY17-18) on automated code generation for high performance graph libraries [collaboration with CMU ECE department]
 - Starting with the GraphBLAS primitives
 - Targeting COTS hardware (CPUs and GPUs)
- DARPA's HIVE (Hierarchical Identify Verify Exploit) BAA released in August
 - References the work of the GraphBLAS Forum
 - Goal: develop a special-purpose graph processing chip

Contact Information

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