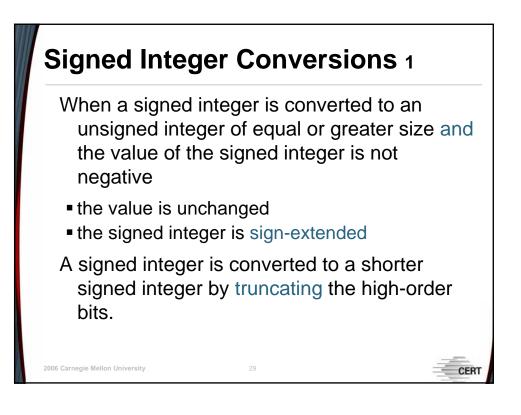
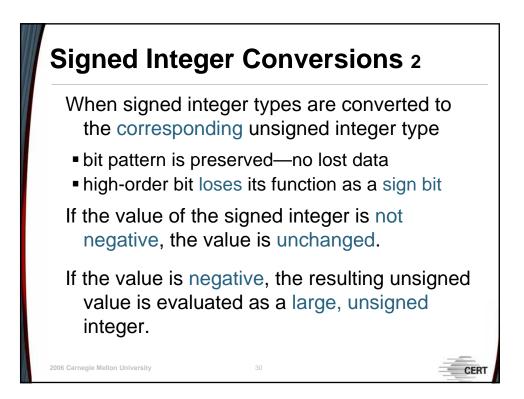
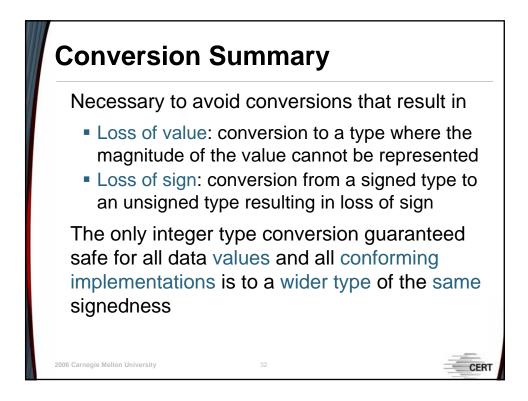


From	То	Method			
unsigned					
char	char	Preserve bit pattern; high-order bit becomes sign bit			
char	short	Zero-extend			
char	long	Zero-extend			
char	unsigned short	Zero-extend			
char	unsigned long	Zero-extend			
short	char	Preserve low-order byte			
short	short	Preserve bit pattern; high-order bit becomes sign bit			
short	long	Zero-extend			
short	unsigned char	Preserve low-order byte			
long	char	Preserve low-order byte			
long	short	Preserve low-order word			
long	long	Preserve bit pattern; high-order bit becomes sign bit			
long	unsigned char	Preserve low-order byte			
long	unsigned short	Preserve low-order word			
2006 Carnegie Mellon University Key: Lost data Misinterpreted data					

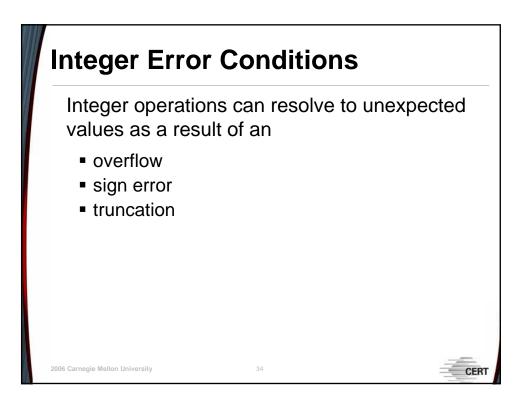


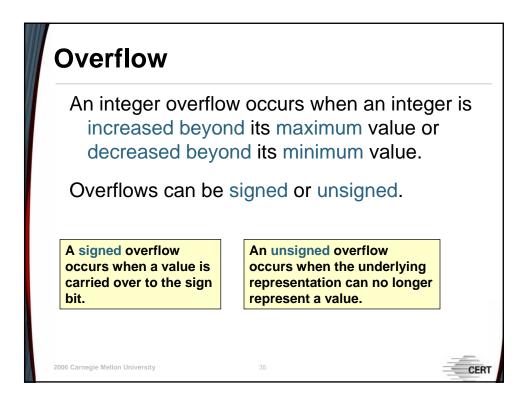


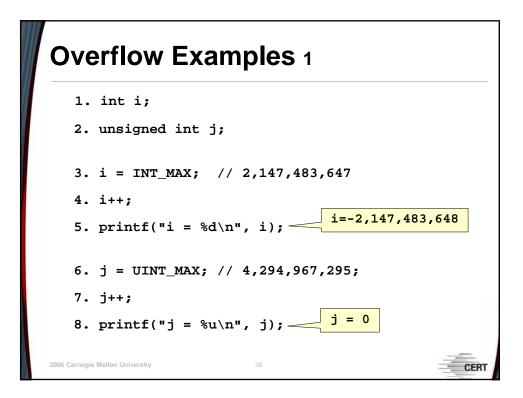
From	То	Method		
char	short	Sign-extend		
char	long	Sign-extend		
char	unsigned char	Preserve pattern; high-order bit loses function as sign bit		
char	unsigned short	Sign-extend to short; convert short to unsigned short		
char	unsigned long	Sign-extend to long; convert long to unsigned long		
short	char	Preserve low-order byte		
short	long	Sign-extend		
short	unsigned char	Preserve low-order byte		
short	unsigned short	Preserve bit pattern; high-order bit loses function as sign bit		
short	unsigned long	Sign-extend to long; convert long to unsigned long		
long	char	Preserve low-order byte		
long	short	Preserve low-order word		
long	unsigned char	Preserve low-order byte		
long	unsigned short	Preserve low-order word		
long	unsigned long	Preserve pattern; high-order bit loses function as sign bit		
2006 Carnegie Mellon University Key: Lost data Misinterpreted data				

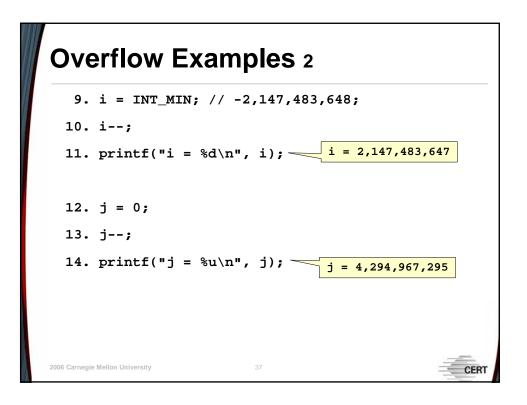


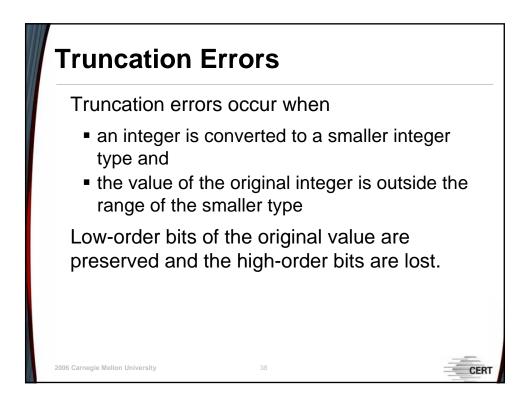
Integer Section	on Agen	da		
Representation				
Types				
Conversions				
Error conditions				
Operations				
2006 Carnegie Mellon University	33		CERT	

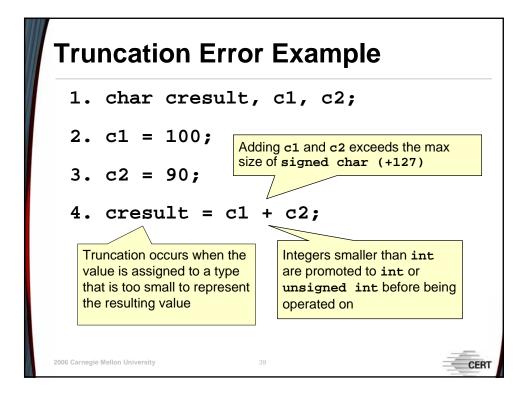


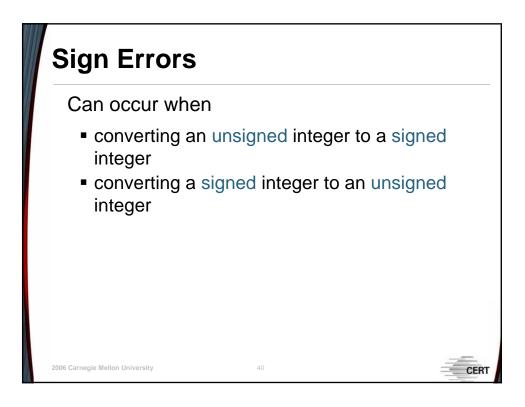


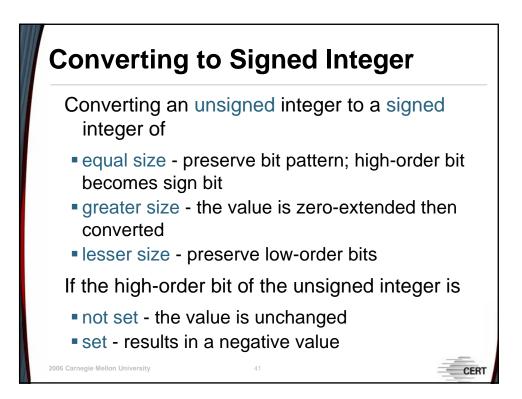


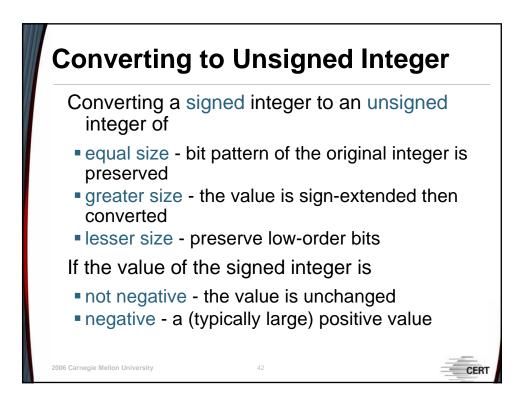


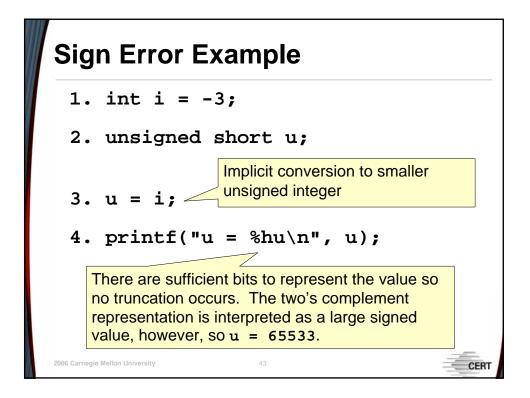


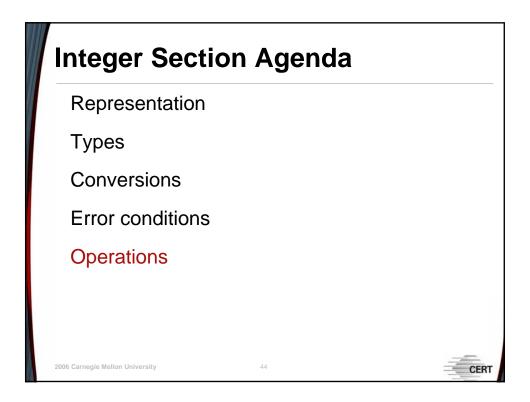


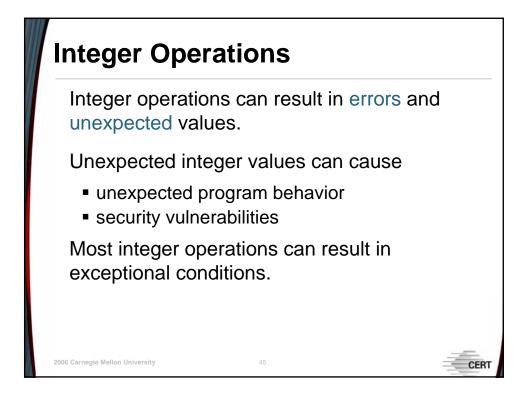


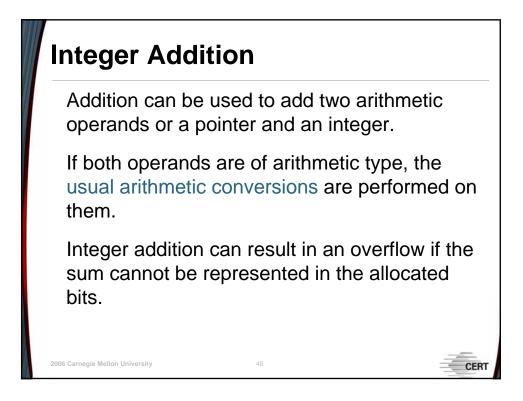


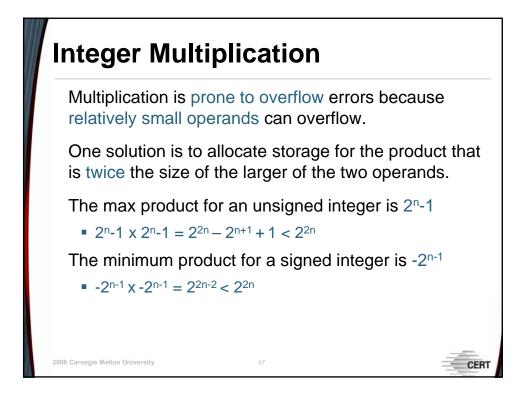


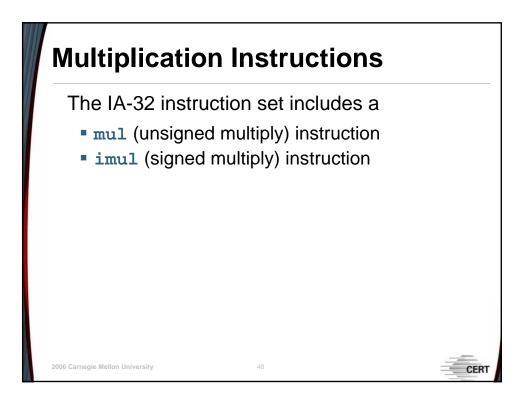


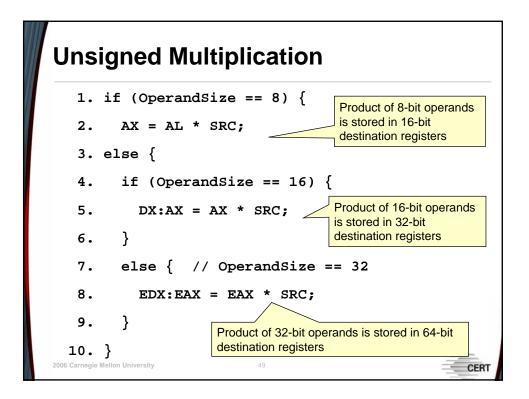


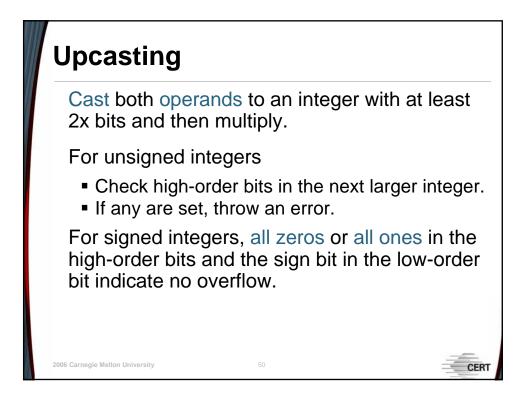


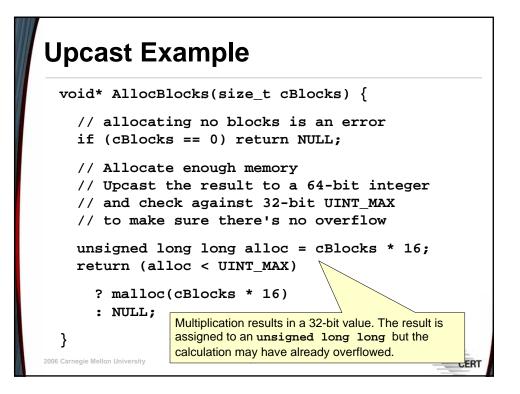


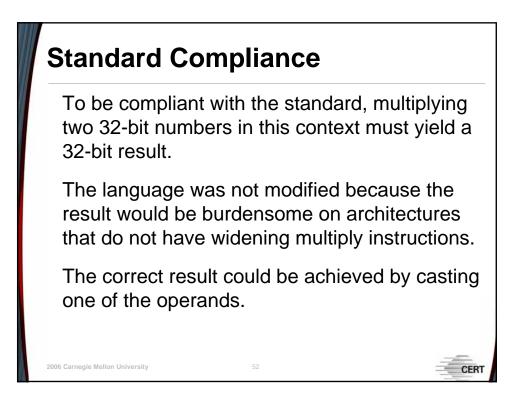


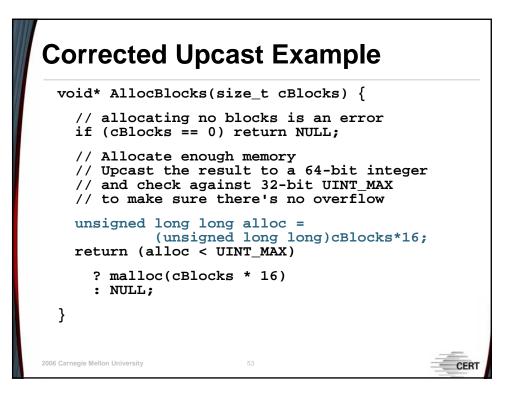


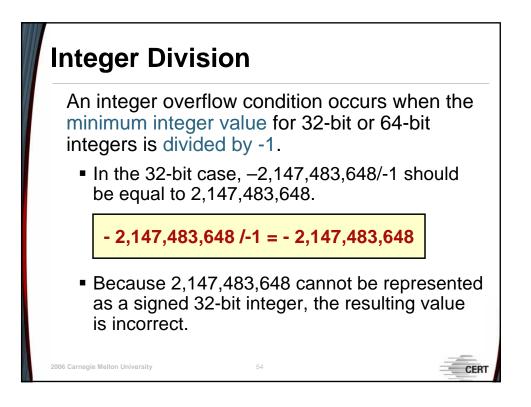












Error Detection

2006 Carnegie Mellon University

The Intel division instructions do not set the overflow flag.

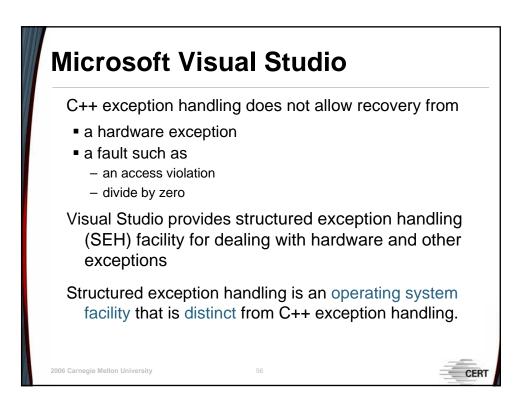
A division error is generated if

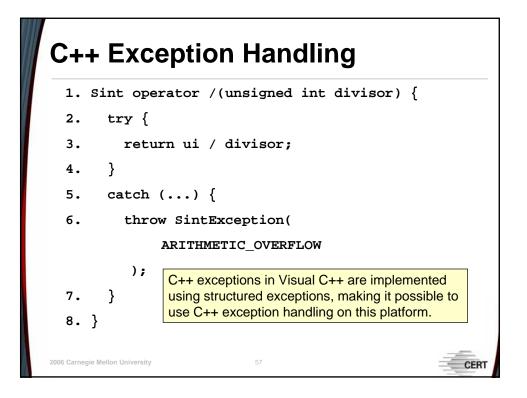
- the source operand (divisor) is zero
- the quotient is too large for the designated register

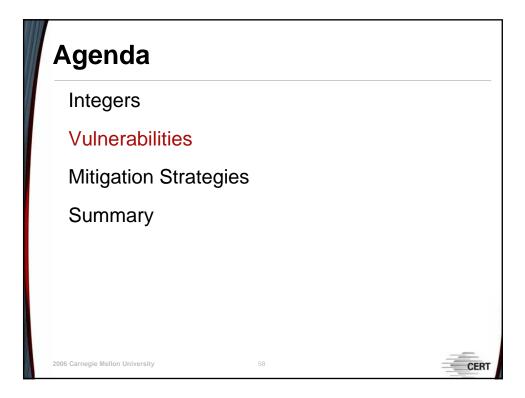
A divide error results in a fault on interrupt vector 0.

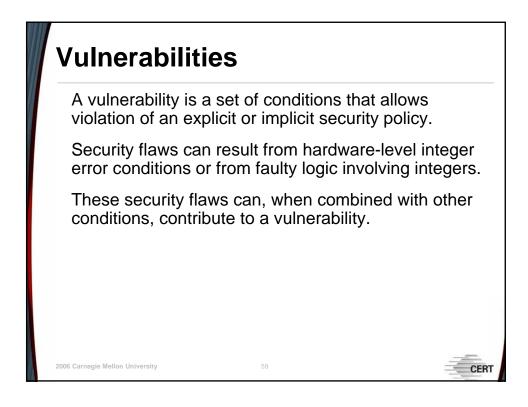
When a fault is reported, the processor restores the machine state to the state before the beginning of execution of the faulting instruction.

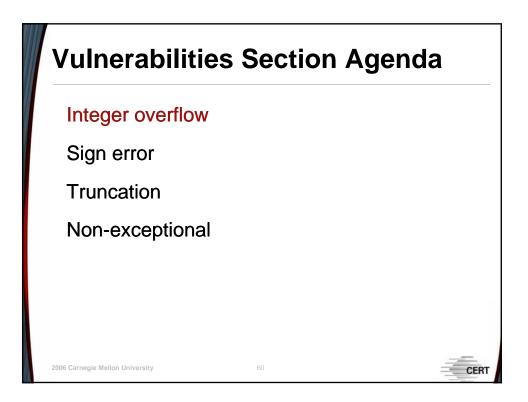
CERT











JPEG Example

2006 Carnegie Mellon University

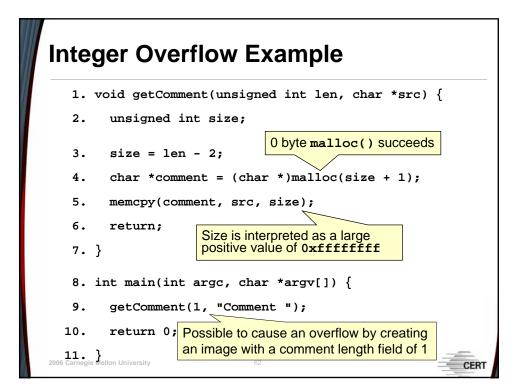
Based on a real-world vulnerability in the handling of the comment field in JPEG files.

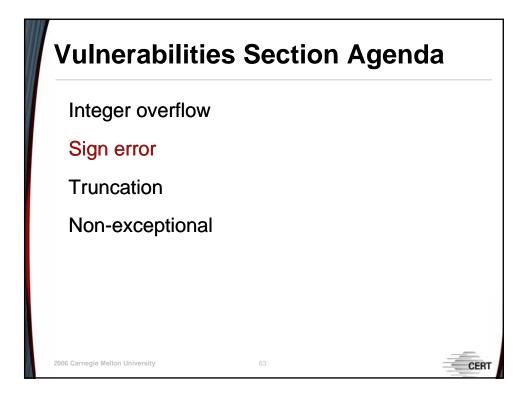
Comment field includes a two-byte length field indicating the length of the comment, including the two-byte length field.

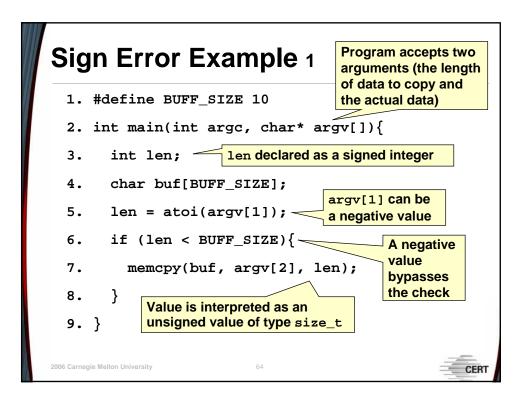
To determine the length of the comment string (for memory allocation), the function reads the value in the length field and subtracts two.

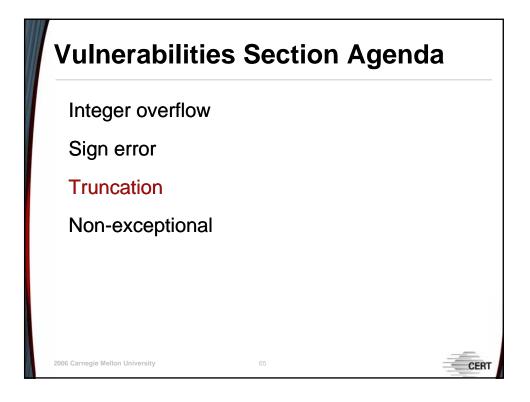
The function then allocates the length of the comment plus one byte for the terminating null byte.

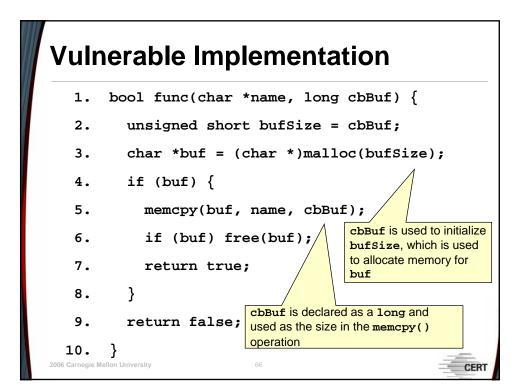
CERT

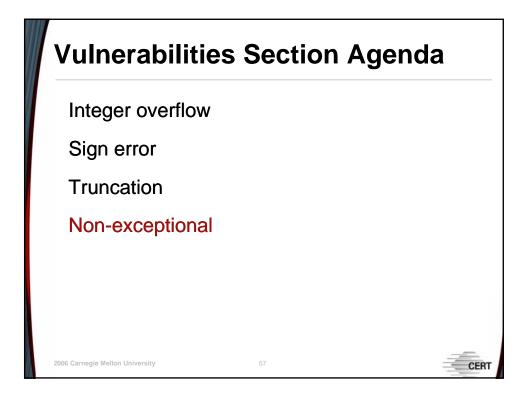


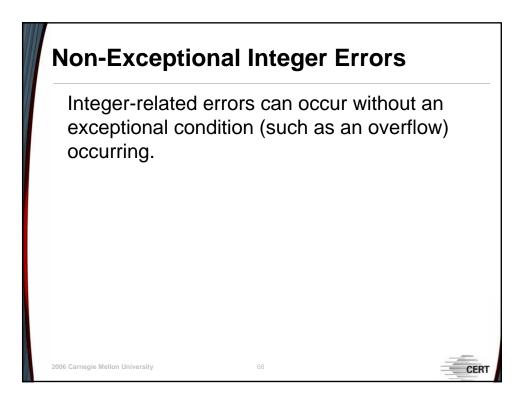


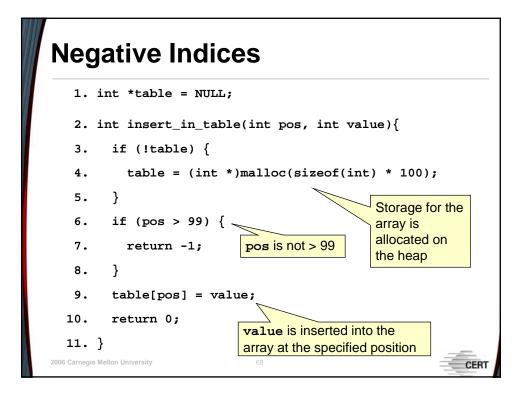


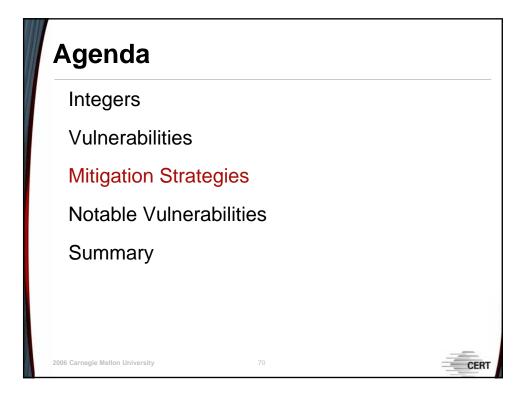


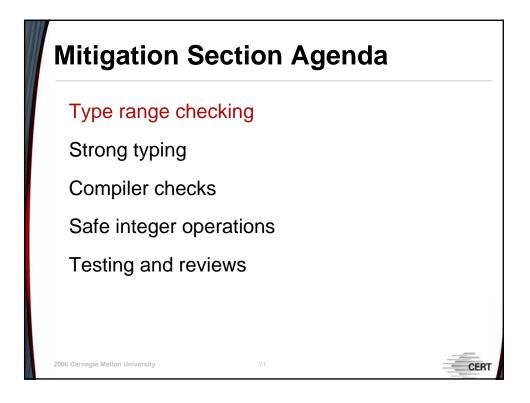


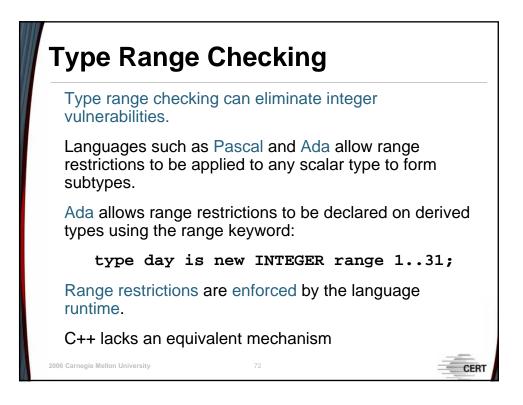


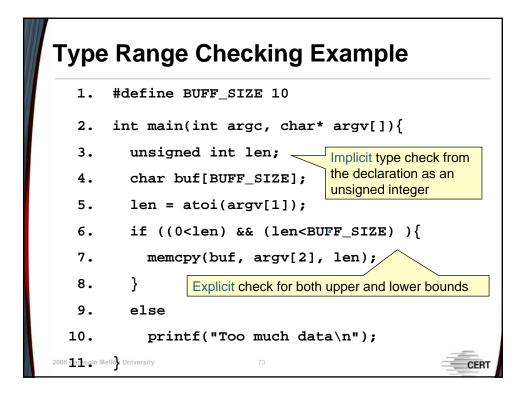


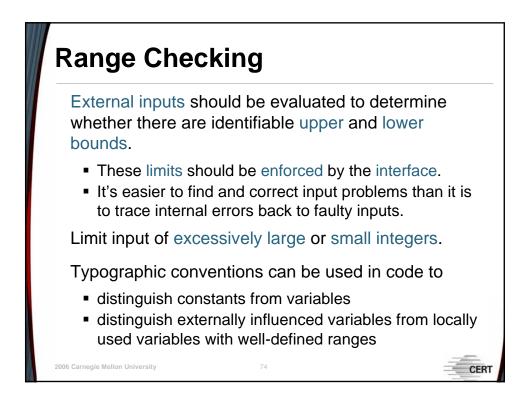


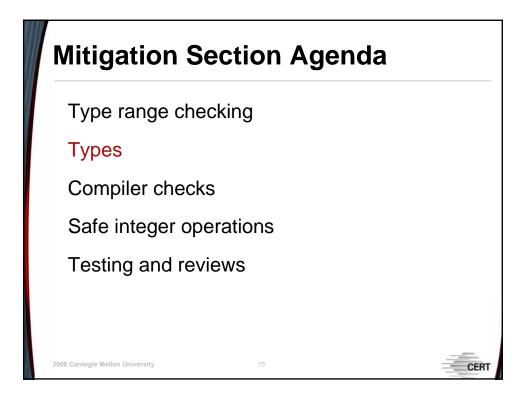


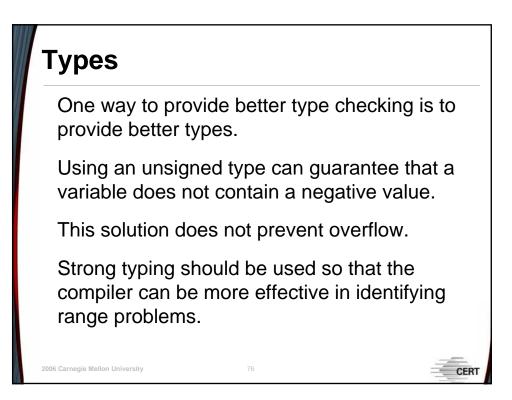












Problem: Representing Object Size

```
Really bad:
short total = strlen(argv[1])+ 1;
Better:
size_t total = strlen(argv[1])+ 1;
Better still:
rsize_t total = strlen(argv[1])+ 1;
```

